



CUSTOMER ADVISORY

ADV2224

Intel® Arria® 10 & Intel® Cyclone® 10 Device Updates

Description:

Intel® is notifying customers of important updates to Intel® Arria® 10 and Intel® Cyclone® 10 Devices.

Refer to Table 1 for the list of major updates and further details.

Table 1

#	Link to Updated Documents	Update Details
1)	Intel® Arria® 10 SX Device Errata and Design Recommendations . Refer to Page 20.	Added a new erratum: GPIO Output may not meet the On-Chip Series Termination (Rs OCT) without Calibration Resistance Tolerance Specification or Current Strength Expectation.
2)	Intel® Arria® 10 GX/GT Device Errata and Design Recommendations Refer to Page 10.	
3)	Intel® Cyclone® 10 GX Device Errata and Design Guidelines . Refer to Page 7.	

Recommended Actions:

Customers are requested to review the changes and determine the impact on their designs. Refer to the revision history of the updated documents for a complete list and history of updates.

For questions or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected:

Intel Arria 10 SX, GX, GT and Cyclone 10 GX Devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://cdrdv2.intel.com/v1/dl/getContent/740055>

Contact:

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<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Customer Notifications Subscription

If you would like to receive customer notifications by email, please follow the instructions in [ADV 2209](#)

Intel references J-STD-046 guidelines for PCN.

In accordance with J-STD-046, this change is deemed acceptable to the customer if no acknowledgement is received within 30 days from date of notification.

Revision History

Date	Rev	Description
08/16/2022	1.0.0	Initial Release

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Order Part No (OPN)

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