

PCN Number:	20160215004	PCN Date:	02/17/2016
Title:	Datasheet for TPS717-Q1		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is updated as seen in the change revision history below:



TPS717-Q1

SLVSBM4C – SEPTEMBER 2012 – REVISED JANUARY 2016

www.ti.com

Changes from Revision B (December 2014) to Revision C

Page

• Moved AEC-Q100 qualification bullet to first in Features list	1
• Added TI Design	1
• Changed TPS717xx-Q1 to TPS717-Q1 throughout document	1
• Added footnote and C_{IN} , R_2 , and C_{NR} parameters to <i>Recommended Operating Conditions</i> table	6
• Changed V_{FB} parameter in <i>Electrical Characteristics</i> table	7
• Changed $\Delta V_{OUT(\Delta I_{OUT})}$ parameter typical specification in <i>Electrical Characteristics</i> table	7
• Changed units of V_n parameter in <i>Electrical Characteristics</i> table	7
• Deleted UVLO parameter minimum specification from <i>Electrical Characteristics</i> table	7
• Changed T_A to T_J in x-axis of Figure 7 , Figure 10 , and Figure 11	9
• Changed 40 mV/div to 40 mA/div in y-axis of Figure 28	12
• Added last two sentences to <i>Undervoltage Lockout (UVLO)</i> section	15
• Changed last bulleted condition in <i>Normal Operation</i> section	15
• Changed T_J specification in <i>Normal mode</i> row of Table 1	16
• Added last sentence to <i>Input and Output Capacitor Requirements</i> section	17
• Clarified discussion of R_2 in second paragraph of <i>Design Considerations</i> section	19
• Changed first and third paragraphs of <i>Do's and Don'ts</i> section	20

Device Family	Change From:	Change To:
TPS717-Q1	SLVSBM4B	SLVSBM4C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/tps717-q1>

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

TPS71709QDRVRQ1	TPS71718QDRVRQ1	TPS71733QDRVRQ1	TPS71750QDSERQ1
TPS71709QDSERQ1	TPS71725QDRVRQ1	TPS71745QDCKRQ1	
TPS71712QDRVRQ1	TPS71728QDRVRQ1	TPS71745QDRVRQ1	
TPS71715QDRVRQ1	TPS71730QDRVRQ1	TPS71750QDRVRQ1	

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
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