

No. KK40001-02
 Date: Sep 29 2012
 KYOCERA Corporation
 KYOCERA Crystal Device Corporation

To : _____

Advance Notice of Production Condition Change

KYOCERA Corporation		KYOCERA Crystal Device Corporation	
Approved by	Approved by	Checked by	Prepared by
 M.Sato	 M.Morimoto	 R.Mizutani	 Y.Saito

KYOCERA Crystal Device is going to implement a change(s) as described below.
 Please examine the change(s).

Item	Discontinuance notice for KC5032C Series SMD Oscillator		
Item(s) to be changed	·Design ·Material ·Production process ·Place of manufacture ·Distribution channel <u>Others (Production discontinuation)</u>		
Details of change(s)	We have decided to discontinue the production of KC5032C series SMD Oscillator due to following reason. We would like to ask your usual understanding on this decision taking into consideration the situation. Model name : KC5032C Series Last day of order : <u>Oct. 2013</u> Last day of production : <u>Dec. 2013</u> We would like to have an acknowledgement and response for this notice by the <u>Oct. 2012</u> .		
Reason for change	Continuation of production became impossible for the production stoppage of a use package. Although we have considered continuation of production, we will consider it as production stoppage unavoidably.		
Attached information	KC5032C → KC5032A	Change to be implemented on	Sep.2012
Remarks			

Please mark your response, and describe the reason if your response is any of 2. or 3. Your response 1. Acceptable 2. Conditionally acceptable 3. Reserved	Reason (s) : _____ _____ _____ _____ _____ _____	Name of your company		
		Division in charge		
		Approved by	Checked by	Filled in by

Please fill the above with your response, and return this form to KYOCERA Crystal Device Corporation

This column is filled by Crystal Device for final confirmation	Engineering Section	Quality Assurance Section	Procurement Department	Sales Department
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(1) KC5032C , KC5032A Attached information

CMOS Load [pF]	Supply Voltage [V]	Frequency [MHz]	Page	KC5032C	KC5032A
15	1.8	1.8 – 50	3	KC5032C-C1 (1.8V)	KC5032A-CM (1.8V)
	2.5	1.8 – 50	4	KC5032C-C2 (2.5V)	KC5032A-CM (2.5V)
		50.0001 – 135	5	KC5032C-C2 (2.5V)	KC5032A-C1 (2.5V)
	3.3	1.8 – 50	6	KC5032C-C3 (3.3V) L_CMOS=15pF	KC5032A-CM (3.3V)
		50.0001 – 135	7	KC5032C-C3 (3.3V) L_CMOS=15pF	KC5032A-C1 (3.3V)
		135.0001 – 170	8	KC5032C-C3 (3.3V) L_CMOS=15pF	KC5032A-C2 (3.3V)
	5.0	1.8 – 50	9	KC5032C-C5 (5.0V) L_CMOS=15pF	KC5032A-CM (5.0V)
	50	3.3	14 – 30	–	KC5032C-C3 (3.3V) L_CMOS=50pF
5.0		1.8 – 50	10	KC5032C-C5 (5.0V) L_CMOS=50pF	KC5032A-HL (5.0V)

① KC5032C-C1 Series

Supply Voltage: 1.8V

Output Frequency Range: 1.8~50MHz

CMOS Load: 15pF

Item	Series		KC5032C-C1 (1.8V)			KC5032A-CM (1.8V)				
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max		
Output Frequency Range	fo	MHz		1.8	39.99		1.8	50		
Frequency Tolerance (Overall)	f_tol	ppm	0	-10~70°C	-50	50	0	-10~70°C	-50	50
			S	-10~70°C	-30	30	S	-10~70°C	-30	30
			U	-10~70°C	-25	25	U	-10~70°C	-25	25
			W	-10~70°C	-20	20	W	-10~70°C	-20	20
			F	-40~85°C	-100	100	F	-40~85°C	-100	100
			G	-40~85°C	-50	50	G	-40~85°C	-50	50
Storage Temperature Range	T_stg	°C		-55	125		-55	125		
Operating Temperature Range	T_use	°C	Standard Specifications	-10	70	Standard Specifications	-10	70		
			Extend(Optional)	-40	85	Extend(Optional)	-40	85		
Max. Supply Voltage	-	V		-0.5	3.6		-0.5	6.5		
Supply Voltage	Vcc	V	Stability 0/S/F	1.71	1.89		1.6	5.5		
			Stability U/G/W	1.75	1.85					
Current Consumption	Icc	mA	1.8 ≤ F ≤ 25MHz	-	3	1.8 ≤ F ≤ 20MHz	-	3.5		
			25 < F ≤ 39.99MHz	-	4	20 < F ≤ 40MHz	-	4.5		
						40 < F ≤ 50MHz	-	5		
Stand-by Current	I_std	uA			10		10			
Symmetry	SYM	%	50%Vcc	45	55	50%Vcc	45	55		
Rise/Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	tr/tf	ns		-	9		-	8		
Low Level Output Voltage	Vol	V	Iol=2.8mA	-	10%Vcc		-	10%Vcc		
High Level Output Voltage	Voh	V	Ioh=-2.8mA	90%Vcc	-		90%Vcc	-		
CMOS Load	CL	pF	C-MOS	-	15	C-MOS	-	15		
Start-up Time	t_str	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10		
1Sigma Jitter	Jsigma	ps		-	8	1.8 ≤ F ≤ 40MHz	-	8		
						40 < F ≤ 50MHz	-	5		
Peak to Peak Jitter	Jpk-pk	ps		-	80	1.8 ≤ F ≤ 40MHz	-	80		
						40 < F ≤ 50MHz	-	40		

② KC5032C-C2 Series

Supply Voltage: 2.5V

Output Frequency Range: 1.8~50MHz

CMOS Load: 15pF

Item	Series		KC5032C-C2 (2.5V)			KC5032A-CM (2.5V)		
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max
Output Frequency Range	f _o	MHz		1.8	50		1.8	50
Frequency Tolerance (Overall)	f _{tol}	ppm	0 -10~70°C	-50	50	0 -10~70°C	-50	50
			S -10~70°C	-30	30	S -10~70°C	-30	30
			U -10~70°C	-25	25	U -10~70°C	-25	25
			W -10~70°C	-20	20	W -10~70°C	-20	20
			F -40~85°C	-100	100	F -40~85°C	-100	100
			G -40~85°C	-50	50	G -40~85°C	-50	50
Storage Temperature Range	T _{stg}	°C		-55	125		-55	125
Operating Temperature Range	T _{use}	°C	Standard Specifications	-10	70	Standard Specifications	-10	70
			Extend(Optional)	-40	85	Extend(Optional)	-40	85
Max. Supply Voltage	-	V		-0.5	7		-0.5	6.5
Supply Voltage	V _{cc}	V	Stability 0/S/F	2.25	2.75		1.6	5.5
			Stability U/G	2.38	2.62			
			Stability W	2.43	2.57			
Current Consumption	I _{cc}	mA	1.8 ≤ F ≤ 20MHz	-	5	1.8 ≤ F ≤ 20MHz	-	4
			20 < F ≤ 40MHz	-	10	20 < F ≤ 40MHz	-	5
			40 < F ≤ 50MHz	-	15	40 < F ≤ 50MHz	-	6
Stand-by Current	I _{std}	uA			10			10
Symmetry	SYM	%	50%V _{cc}	45	55	50%V _{cc}	45	55
Rise/Fall Time (10% V _{cc} to 90% V _{cc} Maximum Loaded)	tr/tf	ns	1.8 ≤ F ≤ 40MHz	-	7		-	7
			40 < F ≤ 50MHz	-	4			
Low Level Output Voltage	V _{ol}	V	I _{ol} =4mA/8mA(40MHz<f ₀)	-	10%V _{cc}		-	10%V _{cc}
High Level Output Voltage	V _{oh}	V	I _{oh} =-4mA/-8mA(40MHz<f ₀)	90%V _{cc}	-		90%V _{cc}	-
CMOS Load	CL	pF	C-MOS	-	15	C-MOS	-	15
Start-up Time	t _{str}	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10
1Sigma Jitter	J _{sigma}	ps	1.8 ≤ F < 40MHz	-	8	1.8 ≤ F ≤ 40MHz	-	8
			40 ≤ F ≤ 50MHz	-	5	40 < F ≤ 50MHz	-	5
Peak to Peak Jitter	J _{pk-pk}	ps	1.8 ≤ F < 40MHz	-	80	1.8 ≤ F ≤ 40MHz	-	80
			40 ≤ F ≤ 50MHz	-	40	40 < F ≤ 50MHz	-	40

③ KC5032C-C2 Series

Supply Voltage: 2.5V

Output Frequency Range: 50.0001~125MHz

CMOS Load: 15pF

Item	Series		KC5032C-C2 (2.5V)			KC5032A-C1 (2.5V)				
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max		
Output Frequency Range	f _o	MHz		50.0001	125		50.0001	135		
Frequency Tolerance (Overall)	f _{tol}	ppm	0	-10~70°C	-50	50	0	-10~70°C	-50	50
			S	-10~70°C	-30	30	S	-10~70°C	-30	30
			U	-10~70°C	-25	25	U	-10~70°C	-25	25
			W	-10~70°C	-20	20	F	-40~85°C	-100	100
			F	-40~85°C	-100	100	G	-40~85°C	-50	50
G	-40~85°C	-50	50							
Storage Temperature Range	T _{stg}	°C		-55	125		-55	125		
Operating Temperature Range	T _{use}	°C	Standard Specifications	-10	70	Standard Specifications	-10	70		
			Extend(Optional)	-40	85	Extend(Optional)	-40	85		
Max. Supply Voltage	-	V		-0.5	7		-0.5	6.5		
Supply Voltage	V _{cc}	V	Stability 0/S/F	2.25	2.75		1.6	3.63		
			Stability U/G	2.38	2.62					
			Stability W	2.43	2.57					
Current Consumption	I _{cc}	mA	50 < F ≤ 60MHz	-	15	50 < F ≤ 85MHz	-	10		
			60 < F ≤ 85MHz	-	20	85 < F ≤ 105MHz	-	15		
			85 < F ≤ 100MHz	-	22	105 < F ≤ 135MHz	-	16		
			100 < F ≤ 125MHz	-	27					
Stand-by Current	I _{std}	uA			10		-	10		
Symmetry	SYM	%	50%V _{cc}	45	55	50%V _{cc}	45	55		
Rise/Fall Time (10% V _{cc} to 90% V _{cc} Maximum Loaded)	tr/tf	ns	50 < F ≤ 85MHz	-	4		-	3		
			85 < F ≤ 125MHz	-	3					
Low Level Output Voltage	V _{ol}	V	I _{ol} =4mA/8mA(40MHz<f ₀)	-	10%V _{cc}		-	10%V _{cc}		
High Level Output Voltage	V _{oh}	V	I _{oh} =-4mA/-8mA(40MHz<f ₀)	90%V _{cc}	-		90%V _{cc}	-		
CMOS Load	CL	pF	C-MOS	-	15	C-MOS	-	15		
Start-up Time	t _{str}	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10		
1Sigma Jitter	J _{sigma}	ps	50 ≤ F ≤ 100MHz	-	5	50 ≤ F ≤ 100MHz	-	5		
			100 < F ≤ 125MHz	-	4	100 < F ≤ 135MHz	-	4		
Peak to Peak Jitter	J _{pk-pk}	ps	50 ≤ F ≤ 100MHz	-	40	50 ≤ F ≤ 100MHz	-	40		
			100 < F ≤ 125MHz	-	30	100 < F ≤ 135MHz	-	30		

④ KC5032C-C3 Series

Supply Voltage: 3.3V

Output Frequency Range: 1.8~50MHz

CMOS Load: 15pF

Item	Series		KC5032C-C3 (3.3V) L_CMOS=15pF			KC5032A-CM (3.3V)		
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max
Output Frequency Range	fo	MHz		1.8	50		1.8	50
Frequency Tolerance (Overall)	f_tol	ppm	0 -10~70°C	-50	50	0 -10~70°C	-50	50
			S -10~70°C	-30	30	S -10~70°C	-30	30
			U -10~70°C	-25	25	U -10~70°C	-25	25
			W -10~70°C	-20	20	W -10~70°C	-20	20
			F -40~85°C	-100	100	F -40~85°C	-100	100
			G -40~85°C	-50	50	G -40~85°C	-50	50
Storage Temperature Range	T_stg	°C		-55	125		-55	125
Operating Temperature Range	T_use	°C	Standard Specifications	-10	70	Standard Specifications	-10	70
			Extend(Optional)	-40	85	Extend(Optional)	-40	85
Max. Supply Voltage	-	V		-0.5	7		-0.5	6.5
Supply Voltage	Vcc	V	Stability 0/S/F	2.97	3.63		1.6	5.5
			Stability U/G	3.14	3.46			
			Stability W	3.20	3.40			
Current Consumption	Icc	mA	1.8 ≤ F ≤ 20MHz	-	10	1.8 ≤ F ≤ 20MHz	-	5
			20 < F ≤ 40MHz	-	15	20 < F ≤ 40MHz	-	6
			40 < F ≤ 50MHz	-	30	40 < F ≤ 50MHz	-	7
Stand-by Current	I_std	uA			10			10
Symmetry	SYM	%	50%Vcc	45	55	50%Vcc	45	55
Rise/Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	tr/tf	ns	1.8 ≤ F ≤ 26MHz	-	10		-	6
			26 < F ≤ 45MHz	-	8			
			45 < F ≤ 50MHz	-	5			
Low Level Output Voltage	Vol	V	Iol=8mA	-	10%Vcc		-	10%Vcc
High Level Output Voltage	Voh	V	Ioh=-8mA	90%Vcc	-		90%Vcc	-
CMOS Load	CL	pF	C-MOS	-	15	C-MOS	-	15
Start-up Time	t_str	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10
1Sigma Jitter	Jsigma	ps	1.8 ≤ F < 40MHz	-	8	1.8 ≤ F ≤ 40MHz	-	8
			40 ≤ F ≤ 50MHz	-	5	40 < F ≤ 50MHz	-	5
Peak to Peak Jitter	Jpk-pk	ps	1.8 ≤ F < 40MHz	-	80	1.8 ≤ F ≤ 40MHz	-	80
			40 ≤ F ≤ 50MHz	-	40	40 < F ≤ 50MHz	-	40

⑤ KC5032C-C3 Series

Supply Voltage: 3.3V

Output Frequency Range: 50.0001~135MHz

CMOS Load: 15pF

Item	Series		KC5032C-C3 (3.3V) L_CMOS=15pF			KC5032A-C1 (3.3V)				
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max		
Output Frequency Range	fo	MHz		50.0001	135		50.0001	135		
Frequency Tolerance (Overall)	f_tol	ppm	0	-10~70°C	-50	50	0	-10~70°C	-50	50
			S	-10~70°C	-30	30	S	-10~70°C	-30	30
			U	-10~70°C	-25	25	U	-10~70°C	-25	25
			W	-10~70°C	-20	20	W	-10~70°C	-20	20
			F	-40~85°C	-100	100	F	-40~85°C	-100	100
			G	-40~85°C	-50	50	G	-40~85°C	-50	50
Storage Temperature Range	T_stg	°C		-55	125		-55	125		
Operating Temperature Range	T_use	°C	Standard Specifications	-10	70	Standard Specifications	-10	70		
			Extend(Optional)	-40	85	Extend(Optional)	-40	85		
Max. Supply Voltage	-	V		-0.5	7		-0.5	6.5		
Supply Voltage	Vcc	V	Stability 0/S/F	2.97	3.63		1.6	3.63		
			Stability U/G	3.14	3.46					
			Stability W	3.20	3.40					
Current Consumption	Icc	mA	50 < F ≤ 60MHz	-	30	50 < F ≤ 85MHz	-	17		
			60 < F ≤ 100MHz	-	35	85 < F ≤ 105MHz	-	19		
			100 < F ≤ 135MHz	-	45	105 < F ≤ 135MHz	-	22		
Stand-by Current	I_std	uA			10		-	10		
Symmetry	SYM	%	50%Vcc	45	55	50%Vcc	45	55		
Rise/Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	tr/tf	ns	50 < F ≤ 100MHz	-	5		-	2.5		
			100 < F ≤ 135MHz	-	2.5					
Low Level Output Voltage	Vol	V	Iol=8mA	-	10%Vcc		-	10%Vcc		
High Level Output Voltage	Voh	V	Ioh=-8mA	90%Vcc	-		90%Vcc	-		
CMOS Load	CL	pF	C-MOS	-	15	C-MOS	-	15		
Start-up Time	t_str	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10		
1Sigma Jitter	Jsigma	ps	50 ≤ F ≤ 100MHz	-	5	50 ≤ F ≤ 100MHz	-	5		
			100 < F ≤ 135MHz	-	4	100 < F ≤ 135MHz	-	4		
Peak to Peak Jitter	Jpk-pk	ps	50 ≤ F ≤ 100MHz	-	40	50 ≤ F ≤ 100MHz	-	40		
			100 < F ≤ 135MHz	-	30	100 < F ≤ 135MHz	-	30		

⑥ KC5032C-C3 Series

Supply Voltage: 3.3V

Output Frequency Range: 135.0001~170MHz

CMOS Load: 15pF

Item	Series		KC5032C-C3 (3.3V) L_CMOS=15pF			KC5032A-C2 (3.3V)		
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max
Output Frequency Range	fo	MHz		135.0001	170		135.0001	170
Frequency Tolerance (Overall)	f_tol	ppm	0 -10~70°C	-50	50	0 -10~70°C	-50	50
			S -10~70°C	-30	30	S -10~70°C	-30	30
			U -10~70°C	-25	25	U -10~70°C	-25	25
			W -10~70°C	-20	20	W -10~70°C	-20	20
			F -40~85°C	-100	100	F -40~85°C	-100	100
			G -40~85°C	-50	50	G -40~85°C	-50	50
Storage Temperature Range	T_stg	°C		-55	125		-55	125
Operating Temperature Range	T_use	°C	Standard Specifications	-10	70	Standard Specifications	-10	70
			Extend(Optional)	-40	85	Extend(Optional)	-40	85
Max. Supply Voltage	-	V		-0.5	7		-0.5	6.5
Supply Voltage	Vcc	V	Stability 0/S/F	2.97	3.63		2.25	3.63
			Stability U/G	3.14	3.46			
			Stability W	3.20	3.40			
Current Consumption	Icc	mA		-	60	2.8 < Vcc ≤ 3.63V		
						135 < F0 ≤ 150MHz	-	21
						150 < F0 ≤ 175MHz	-	25
Stand-by Current	I_std	uA	135 < F ≤ 170MHz		150		-	10
Symmetry	SYM	%	50%Vcc	45	55	50%Vcc	45	55
Rise/Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	tr/tf	ns	135 < F ≤ 170MHz	-	2.5	2.8 < Vcc ≤ 3.63V	-	2.5
Low Level Output Voltage	Vol	V	Iol=8mA	-	10%Vcc		-	10%Vcc
High Level Output Voltage	Voh	V	Ioh=-8mA	90%Vcc	-		90%Vcc	-
CMOS Load	CL	pF	C-MOS	-	15	C-MOS	-	15
Start-up Time	t_str	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10
1Sigma Jitter	Jsigma	ps	135 < F ≤ 170MHz	-	4	135 < F ≤ 170MHz	-	4
Peak to Peak Jitter	Jpk-pk	ps	135 < F ≤ 170MHz	-	30	135 < F ≤ 170MHz	-	30

⑦ KC5032C-C5 Series

Supply Voltage: 5.0V

Output Frequency Range: 1.8~50MHz

CMOS Load: 15pF

Item	Series		KC5032C-C5 (5.0V) L_CMOS=15pF			KC5032A-CM (5.0V)				
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max		
Output Frequency Range	fo	MHz		1.8	50		1.8	50		
Frequency Tolerance (Overall)	f_tol	ppm	0	-10~70°C	-50	50	0	-10~70°C	-50	50
			S	-10~70°C	-30	30	S	-10~70°C	-30	30
			U	-10~70°C	-25	25	U	-10~70°C	-25	25
			F	-40~85°C	-100	100	F	-40~85°C	-100	100
			G	-40~85°C	-50	50	G	-40~85°C	-50	50
Storage Temperature Range	T_stg	°C		-55	125		-55	125		
Operating Temperature Range	T_use	°C	Standard Specifications	-10	70	Standard Specifications	-10	70		
			Extend(Optional)	-40	85	Extend(Optional)	-40	85		
Max. Supply Voltage	-	V		-0.5	7		-0.5	6.5		
Supply Voltage	Vcc	V	Stability 0/S/F	4.5	5.5		1.6	5.5		
			Stability U/G	4.75	5.25					
Current Consumption	Icc	mA	1.8 ≤ F ≤ 20MHz	-	25	1.8 ≤ F ≤ 20MHz	-	7		
			20 < F ≤ 40MHz	-	35	20 < F ≤ 40MHz	-	8		
			40 < F ≤ 50MHz	-	50	40 < F ≤ 50MHz	-	9.5		
Stand-by Current	I_std	uA			20mA		10			
Symmetry	SYM	%	50%Vcc	45	55	50%Vcc	45	55		
Rise/Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	tr/tf	ns	1.8 ≤ F ≤ 26MHz	-	10		-	5		
			26 < F ≤ 50MHz	-	8					
Low Level Output Voltage	Vol	V	Iol=16mA	-	10%Vcc		-	10%Vcc		
High Level Output Voltage	Voh	V	Ioh=-16mA	90%Vcc	-		90%Vcc	-		
CMOS Load	CL	pF	C-MOS	-	15	C-MOS	-	15		
Start-up Time	t_str	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10		
1Sigma Jitter	Jsigma	ps	1.8 ≤ F ≤ 40MHz	-	8	1.8 ≤ F ≤ 40MHz	-	8		
			40 < F ≤ 50MHz	-	5	40 < F ≤ 50MHz	-	5		
Peak to Peak Jitter	Jpk-pk	ps	1.8 ≤ F ≤ 40MHz	-	80	1.8 ≤ F ≤ 40MHz	-	80		
			40 < F ≤ 50MHz	-	40	40 < F ≤ 50MHz	-	40		

⑧ KC5032C-C5 Series

Supply Voltage: 5.0V

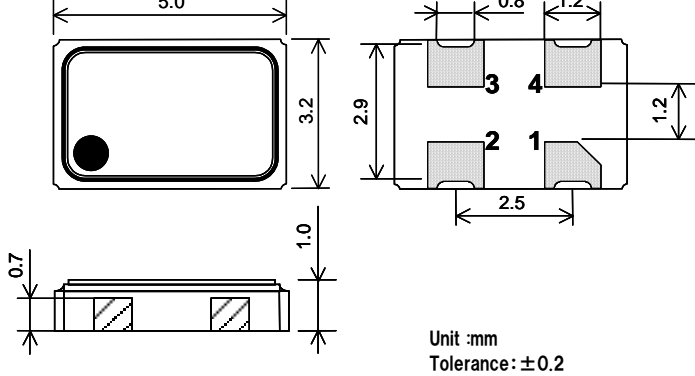
Output Frequency Range: 1.8~50MHz

CMOS Load: 50pF

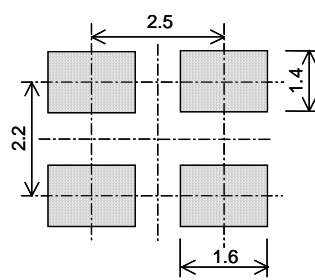
Item	Series		⑥KC5032C-C5 (5.0V) L_CMOS=50pF			KC5032A-HL (5.0V)				
	Symbol	Units	Conditions	Min	Max	Conditions	Min	Max		
Output Frequency Range	f _o	MHz		1.8	50		1.8	50		
Frequency Tolerance (Overall)	f _{tol}	ppm	0	-10~70°C	-50	50	0	-10~70°C	-50	50
			S	-10~70°C	-30	30	S	-10~70°C	-30	30
			U	-10~70°C	-25	25	U	-10~70°C	-25	25
			F	-40~85°C	-100	100	F	-40~85°C	-100	100
			G	-40~85°C	-50	50	G	-40~85°C	-50	50
Storage Temperature Range	T _{stg}	°C		-55	125		-55	125		
Operating Temperature Range	T _{use}	°C	Standard Specifications	-10	70	Standard Specifications	-10	70		
			Extend(Optional)	-40	85	Extend(Optional)	-40	85		
Max. Supply Voltage	-	V		-0.5	7		-0.5	6.5		
Supply Voltage	V _{cc}	V	Stability 0/S/F	4.5	5.5		4.5	5.5		
			Stability U/G	4.75	5.25					
Current Consumption	I _{cc}	mA	1.8 ≤ F ≤ 20MHz	-	25	1.8 ≤ F ≤ 50MHz	-	10		
			20 < F ≤ 40MHz	-	35					
			40 < F ≤ 50MHz	-	50					
Stand-by Current	I _{std}	uA			20mA		10			
Symmetry	SYM	%	50%V _{cc}	45	55	50%V _{cc}	45	55		
Rise/Fall Time (10% V _{cc} to 90% V _{cc} Maximum Loaded)	tr/tf	ns	1.8 ≤ F ≤ 26MHz	-	10	1.8 ≤ F ≤ 50MHz	-	10		
			26 < F ≤ 50MHz	-	8					
Low Level Output Voltage	V _{ol}	V	I _{ol} =16mA	-	10%V _{cc}		-	10%V _{cc}		
High Level Output Voltage	V _{oh}	V	I _{oh} =-16mA	90%V _{cc}	-		90%V _{cc}	-		
CMOS Load	CL	pF	C-MOS	-	50	C-MOS	-	50		
Start-up Time	t _{str}	ms	Minimum operating voltage to be 0 sec.	-	10	Minimum operating voltage to be 0 sec.	-	10		
1Sigma Jitter	J _{sigma}	ps	1.8 ≤ F ≤ 40MHz	-	8	1.8 ≤ F ≤ 40MHz	-	8		
			40 < F ≤ 50MHz	-	5	40 < F ≤ 50MHz	-	5		
Peak to Peak Jitter	J _{pk-pk}	ps	1.8 ≤ F ≤ 40MHz	-	80	1.8 ≤ F ≤ 40MHz	-	80		
			40 < F ≤ 50MHz	-	40	40 < F ≤ 50MHz	-	40		

KC5032C, KC5032A Attached information

Dimensions (KC5032C)



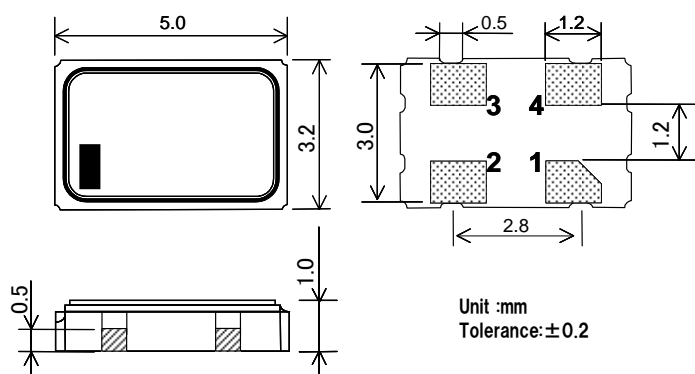
Recommended Land Pattern (KC5032C) Unit:mm



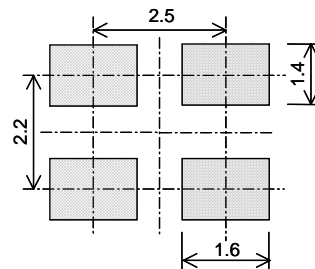
	Pad Connections
1	Enable/Disable
2	Case GND
3	Output
4	VCC

Enable/Disable Condition	
Pad1	Pad3 (Output)
OPEN	Active
"H" Level	Active
"L" Level	High Z

Dimensions (KC5032A)



Recommended Land Pattern (KC5032A) Unit :mm



	Pad Connections
1	Enable/Disable
2	Case GND
3	Output
4	VCC

Enable/Disable Condition	
Pad1	Pad3 (Output)
OPEN	Active
"H" Level	Active
"L" Level	High Z

