

Product / Process Change Notification



N° 2020-041-A

correction

Dear Customer,

Please find attached our INFINEON Technologies PCN:

Dual flexible wafer manufacturing site strategy: expansion of several Gen 5 HVIC products produced at Newport Wafer Fab Limited

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before ~~1st April 2020~~. **4th March 2020**.
- Infineon aligns with the widely-recognized JEDEC STANDARD “**JESD46**“, which stipulates:
“**Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change.**”

Your prompt reply will help Infineon Technologies to assure a smooth and well executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Infineon Technologies AG
Postal Address Headquarters: Am Campeon 1-15, D-85579 Neubiberg, Phone +49 (0)89 234-0
Chairman of the Supervisory Board: Dr. Wolfgang Eder
Management Board: Dr. Reinhard Ploss (CEO), Dr. Helmut Gassel, Jochen Hanebeck, Dr. Sven Schneider
Registered Office: Neubiberg
Commercial Register: München HRB 126492

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► **Products affected:**

Please refer to attached affected product list 1_cip20041_a

► **Detailed Change Information:**

Subject: Enabling Newport Wafer Fab Limited for additional Gen 5 HVIC products

Reason: Expansion of current wafer production capacity to assure continuity of supply and enable flexible manufacturing.

Description:	<u>Old</u>	<u>New</u>
Production Site	Gen 5 HVIC technology released and in production at: <ul style="list-style-type: none">■ NXP Oak Hill■ Newport Wafer Fab Limited (previously announced to be decommissioned)	Gen 5 HVIC technology released and in production at: <ul style="list-style-type: none">■ NXP Oak Hill■ Newport Wafer Fab Limited will extend supply

► **Product Identification:**

Wafer fab origins will not be distinguished through the external part marking or shipping labels, however, based on the assembly lot code and marked on the device. IFX internal systems will be able to trace the site of the wafer lots used to produce the final product.

► **Impact of Change:**

No changes to form, fit or function. The package outline remains the same. Datasheet specifications will not be changed. The package bill of material and the location for assembly and final test will not change.

► **Attachments:**

Affected product list 1_cip20041_a

► **Time Schedule:**

- | | |
|-------------------------------|--------------|
| ■ Final qualification report: | upon Request |
| ■ First samples available: | upon Request |
| ■ Intended start of delivery: | 01-June-2020 |

If you have any questions, please do not hesitate to contact your local Sales office.

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Sales name	SP number	OPN	Package
AUIRS1170STR	SP001609062	AUIRS1170STR	PSOP 8
AUIRS2012STR	SP001514248	AUIRS2012STR	SOIC 8N
AUIRS20302STR	SP001514368	AUIRS20302STR	SOIC 28W
AUIRS2113STR	SP001512418	AUIRS2113STR	SOIC 16W
AUIRS2123STR	SP001512098	AUIRS2123STR	SOIC 8N
AUIRS21271STR	SP001511982	AUIRS21271STR	SOIC 8N
AUIRS2127STR	SP001512314	AUIRS2127STR	SOIC 8N
AUIRS21811STR	SP001511172	AUIRS21811STR	SOIC 8N
AUIRS21814STR	SP001511734	AUIRS21814STR	SOIC 14N
AUIRS2181STR	SP001511506	AUIRS2181STR	SOIC 8N
AUIRS2191STR	SP001512306	AUIRS2191STR	PG-DSO-16
AUIRS2301STR	SP001512090	AUIRS2301STR	SOIC 8N
AUIRS2336STR	SP001512298	AUIRS2336STR	SOIC 28W
AUIRS4427STR	SP001512820	AUIRS4427STR	SOIC 8N
AUXDI2118STR	SP001512290	AUXDI2118STR	SOIC 8N